

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: N. YAMAMOTO, et al.

Serial No: To be assigned

Filed: January 21, 2004

Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
METHOD FOR MANUFACTURING THE SAME

Group: n/a

Examiner: n/a

REAFFIRMATION OF CLAIM FOR PRIORITY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

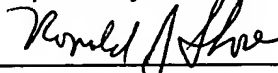
January 21, 2004

Sir:

Applicants reaffirm their claim for priority under 35 U.S.C. §119 based on Japanese Patent Application No. 11-158089 filed June 4, 1999. A certified copy of said Japanese Application was filed in the parent application serial no. 09/577,671 on May 25, 2000.

To the extent necessary, Applicants petition for an extension of time under 37 CFR §1.136. Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 01-2135 (Case No. 501.38505CX1) and please credit any excess fees to such deposit account.

Respectfully submitted,



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RJS/kmh
Attachments